

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

KUKATPALLY-HYDERABAD-500085
EXAMINATION BRANCH

M.TECH-I SEMESTER -R19 REGULATION- REGULAR EXAMINATIONS JANUARY-2020

T I M E T A B L E

TIME: 10:00 AM. TO 1:00 PM.

NB
21/12/19

Course	08-01-2020 WEDNESDAY	10-01-2020 FRIDAY	13-01-2020 MONDAY	18-01-2020 SATURDAY	21-01-2020 TUESDAY	23-01-2020 THURSDAY	25-01-2020 SATURDAY	28-01-2020 TUESDAY	
TRANSPORTATION ENGINEERING - 22	ELECTIVE-I Traffic Engineering	Urban Policy & Planning	Pavement Material Characterization	ELECTIVE-I Modeling & Simulation	Elective-II Advanced Concrete Technology Remote sensing & GIS Airport Engineering	MC Research Methodology and IPR	ELECTIVE-I Road Safety Engineering	---	
	ELECTIVE-I Design of Fault Tolerant Systems			ELECTIVE-I System-on-Chip Architecture	ELECTIVE-I VLSI Technology and Design		ELECTIVE-II Wireless MIMO Communications Optical Communications and Networks	ELECTIVE-II Coding Theory and Techniques	Wireless Communication s and Networks
	ELECTIVE-I Design of Fault Tolerant Systems			Advanced Digital System Design	ELECTIVE-I System-on-Chip Architecture		ELECTIVE-I VLSI Technology and Design	ELECTIVE-II Wireless MIMO Communications Optical Communications and Networks	ELECTIVE-II Coding Theory and Techniques

DATE: 21/11/2019

Sd/-

CONTROLLER OF EXAMINATIONS

NOTE:

- (I). ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY
(II). EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES. THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

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EXAMINATION BRANCH

M.TECH-I SEMESTER -R17 REGULATION- SUPPLEMENTARY EXAMINATIONS JANUARY-2020

TIMETABLE

NB
KR
21/12/19

TIME: 10:00 AM TO 1:00 PM

Course	08-01-2020 WEDNESDAY	10-01-2020 FRIDAY	13-01-2020 MONDAY	18-01-2020 SATURDAY	21-01-2020 TUESDAY	23-01-2020 THURSDAY	25-01-2020 SATURDAY	28-01-2020 TUESDAY	
ELECTRONICS AND COMMUNICATI ON ENGINEERING - 70	Digital System Design	ELECTIVE-I	ELECTIVE-I	ELECTIVE-II	Advanced Digital Signal Processing	OE-I	Coding Theory and Techniques	---	
		Detection and Estimation Theory	Digital Signal Processors and Architectures	Real Time Operating Systems		Computer Oriented Numerical Methods			
			Speech Signal Processing	Adhoc Wireless Sensor Networks		Renewable Energy Systems			
						Advanced Computer Architecture			Electrical Installation And Safety
						Optimization Techniques And Applications			
				Fundamentals Of Cyber Security					

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EXAMINATION BRANCH

M.TECH-II SEMESTER -R17 REGULATION SUPPLEMENTARY EXAMINATIONS JANUARY-2020

T I M E T A B L E

TIME : 10:00 AM TO 1:00 PM

MB
21/12/19

COURSE	09-01-2020 THURSDAY	11-01-2020 SATURDAY	17-01-2020 FRIDAY	20-01-2020 MONDAY	22-01-2020 WEDNESDAY	24-01-2020 FRIDAY	27-01-2020 MONDAY	29-01-2020 WEDNESDAY	
Electronics and Communication Engineering-70	E-IV Optical Communication s and Networks	Elective -IV Design of Fault Tolerant Systems	Embedded System Design	E-IV Image and Video Processing	OE-II Finite Element Method	(Elective –III) Spread Spectrum Communications	System on Chip Architectures	Wireless Communicatio ns and Networks	
					Optimization Techniques				
					Energy From Waste				
					Distributed Generation And Microgrid				Network Security And Cryptography
					Reliability Engineering				Adaptive Signal Processing
					Engineering Research Methodology				
Machine Learning									

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